

Customer No.: 31561 Docket No. 9265-US-PA Application No.: 10/604,042

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Application No.

: Lai et al. : 10/604,042

Filed

: 2003/06/24

For

: THREE-DIMENSIONAL MEMORY STRUCTURE AND

MANUFACTURING METHOD THEREOF

Art Unit

: 2812

Examiner

: KENNEDY, JENNIFER M

TRANSMITTAL LETTER

002-1-703-872-9306

(Via fax: 3 pages, followed by confirmation copy via courier)

Assistant Commissioner for Patents Arlington, Virginia 22202

Dear Sirs,

In response to the Office Action dated April 5, 2004, please find the relevant paper in response to paper No. 20040401. Following the fax transmission, a hard copy via courier will also be forwarded to the Office.

Enclosed documents via courier will include:

\boxtimes	Response to Restriction Requirement in (1) pages
\boxtimes	Fax confirmation report
\boxtimes	Prepaid return postcard
	Extension fee

I believe that no fee is incurred. However, the Commissioner is authorized to charge any fees required in connection with the filing of this paper to account No. 50-2620 (Order No.: 9265-US-PA)

Thank you for your assistance in the subject matter. If you have any questions, please feel free to contact me.

Customer No.: 31561 Docket No. 9265-US-PA Application No.: 10/604,042

Respectfully Submitted,
JIANQ CHYUN Intellectual Property Office

Date: \$\frac{1}{26}, 270\frac{1}{20}

y: Halinda

Registration No.: 46,863

Please send future correspondence to:

7F. -1, No. 100, Roosevelt Rd.,

Sec. 2, Taipei 100, Taiwan, R.O.C.

Tel: 886-2-2369 2800

Fax: 886-2-2369 7233 / 886-2-2369 7234

TRANSACTION REPORT

APR-26-2004 MON 14:48

FOR: JCIPO Taiwan

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Patent

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Examiner

: KENNEDY, JENNIFER M

RESPONSE TO RESTRICTION REQUIREMENT

ASSISTANT COMISSIONER FOR PATENTS Arlington, VA 22202

Dear Sirs,

In response to the Office Action, mailed on April 5, 2004, Applicant would like to elect Group I, claims 1-12, drawn to a semiconductor device, classified in class 257, subclass 530+. Please cancel Group II, claims 13-24, drawn to the method of making a semiconductor device, classified in class 438, subclass 131+ without waiver, prejudice or disclaimer.

Thank you for your assistance in the subject matter. If you have any questions, please feel free to contact me.

Respectfully Submitted, JIANQ CHYUN IP Office

Date: April 26, 2004

Polindo I

Registration No.: 46,863

Please send future correspondence to:

7F. -1, No. 100, Roosevelt Rd.,

Sec. 2, Taipei 100, Taiwan, R.O.C.

Tel: 886-2-2369 2800

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